

Receipt

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: David M. Fried, et al.

Serial No.: 10/645,063

Filing Date: 08/21/2003

Art Unit: 2812

Dkt. No.: BUR920010082US2

Title: Multiple-plane FinFET CMOS



Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

CORRECTION OF FILING RECEIPT

Sir:

Applicants respectfully request correction of the filing receipt for this case as indicated on the attached copy of the filing receipt.

Consideration of the foregoing in relation to this patent application is respectfully requested.

Respectfully submitted,

Date: 11/25/2003

Jack P. Friedman

Jack Friedman

Reg. No. 44,688

SCHMEISER, OLSEN & WATTS

3 Lear Jet Lane, Suite 201

Latham, NY 12110

(518) 220-1850

I HEREBY CERTIFY THAT THE CORRESPONDENCE TO WHICH THIS STATEMENT IS AFFIXED IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE, POSTAGE PAID, AS FIRST CLASS MAIL, IN AN ENVELOPE ADDRESSED TO ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231 ON 11/25/2003

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/645,063	08/21/2003	2812	750	BUR920010082US2	9	6	1

30449
SCHMEISER, OLSEN + WATTS
SUITE 201
3 LEAR JET
LATHAM, NY 12033



CONFIRMATION NO. 6534

FILING RECEIPT



OC000000011323192

Date Mailed: 11/20/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Essex Junction
David M. Fried, Williston VT;
Edward J. Nowak, Essex Junction, VT;

Domestic Priority data as claimed by applicant

This application is a DIV of 10/011,846 12/04/2001 PAT 6,657,259

Foreign Applications

If Required, Foreign Filing License Granted: 11/20/2003

Projected Publication Date: 02/26/2004

Non-Publication Request: No

Early Publication Request: No

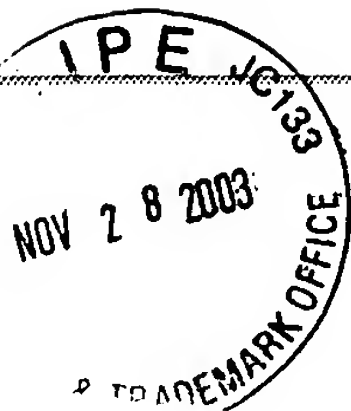
Title

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Multiple-plane FinFET CMOS

Preliminary Class

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